TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC125AP,TC74HC125AF,TC74HC125AFN TC74HC126AP,TC74HC126AF

TC74HC125AP/AF/AFN Quad Bus Buffer TC74HC126AP/AF Quad Bus Buffer

The TC74HC125A/126A are high speed CMOS QUAD BUS BUFFERs fabricated with silicon gate C²MOS technology.

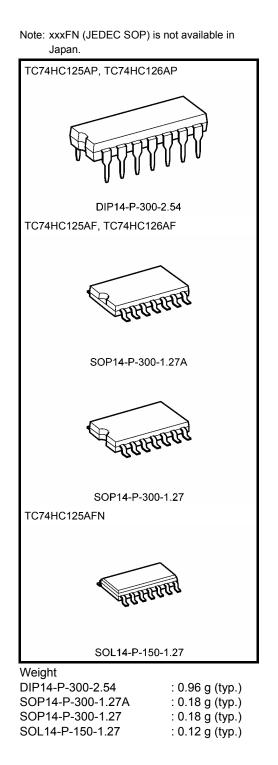
They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The TC74HC125A requires the 3-state control input \overline{G} to be set high to place the output into the high impedance state, whereas the TC74HC126A requires the control input to be set low to place the output into high impedance.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 10 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \ \mu A \ (max)$ at $Ta = 25^{\circ}C$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 6 \text{ mA} (\text{min})$
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Pin and function compatible with 74LS125/126

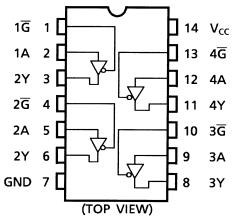


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4G

Pin Assignment





IEC Logic Symbol

TC74HC125A

1G 1A	$\frac{(1)}{(2)}$	EN	⊳	⊽	<u>(3)</u> 1Y
1 <u>A</u> 2G 2 <u>A</u> 3G	(4) (5)				<u>(6)</u> 2Y
3G 3A	(10) (9) (13)				<u>(8)</u> 3Y
3 <u>A</u> 4G 4A	(13)				<u>(11)</u> 4Y

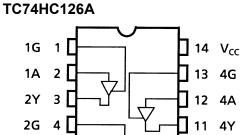
Truth Table

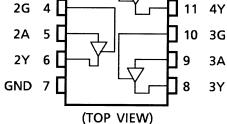
TC74HC125A

Inp	uts	Output
ĪG	А	Y
Н	Х	Z
L	L	L
L	Н	Н

X: Don't care

Z: High impedance





TC74HC126A

1G 1A	7-1	EN	⊳	⊽	(<u>3)</u> 1Y
2G 2A 3G	(4) (5) (10)				<u>(6)</u> 2Y
3A	(<u>10)</u> (<u>9)</u> (13)				(<u>8)</u> 3Y
4G 4A	(12)				<u>(11)</u> 4Y

TC74HC126A

Inp	uts	Output
G	А	Y
L	Х	Z
Н	L	L
Н	Н	Н

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	–0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±35	mA
DC V _{CC} /ground current	ICC	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V_{CC} = 4.5 V)	ns
		0 to 400 ($V_{CC} = 6.0 \text{ V}$)	

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		-	Ta = 25°0	0	Ta = -40 to 85°C		Unit	
Characteriolice	Cymbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	
				2.0	1.50			1.50		
High-level input voltage	VIH		_	4.5	3.15		—	3.15	—	V
Ũ				6.0	4.20		_	4.20	—	
				2.0	_		0.50	—	0.50	
Low-level input voltage	VIL		_	4.5	—		1.35	—	1.35	V
Ũ				6.0	_		1.80	—	1.80	
				2.0	1.9	2.0	—	1.9	—	
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	4.5	4.4	4.5	—	4.4	—	V
High-level output voltage				6.0	5.9	6.0	—	5.9	—	
Ũ			I _{OH} = -6 mA	4.5	4.18	4.31	_	4.13	_	
			I _{OH} = -7.8 mA	6.0	5.68	5.80	—	5.63	Max — — 0.50 1.35	
				2.0	_	0.0	0.1	_	0.1	
		VIN	I _{OL} = 20 μA	4.5	—	0.0	0.1	—	0.1	
Low-level output voltage	V _{OL}	= V _{IH} or		6.0	—	0.0	0.1	—	0.1	V
Ũ		VIL	I _{OL} = 6 mA	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 7.8 mA	6.0	—	0.18	0.26	—	0.33	
3-state output off-state current	I _{OZ}	V _{IN} = V _{IF} V _{OUT} = V	_I or V _{IL} / _{CC} or GND	6.0	_	_	±0.5	_	±5.0	μA
Input leakage current	I _{IN}	$V_{IN} = V_C$	_C or GND	6.0	_	_	±0.1		±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_C$	_C or GND	6.0		_	4.0		40.0	μA

Characteristics	Symbol	Test Condition		-	Га = 25°С)	Ta = -40 to 85°C		Unit		
Characteristics	Symbol		CL (pF)	V _{CC} (V)	Min	Тур.	Max	Min	Max		
	tтLн			2.0	_	20	60		75		
Output transition time	t _{THL}	—	50	4.5	—	6	12	—	15	ns	
	41AL			6.0	_	5	10	_	13		
				2.0	—	30	90	—	115		
			50	4.5	—	11	18	—	23		
Propagation delay	t _{pLH}			6.0	—	10	15	—	20	ns	
time	t _{pHL}			2.0	_	42	130	_	165	115	
			150	4.5	—	14	26	—	33		
				6.0	_	12	22	_	28		
	t _p ZL t _p ZH	$R_L = 1 \ k\Omega$		2.0		30	90		115		
			50	4.5	_	11	18	_	23	- ns	
Output enable time				6.0	—	10	15	—	20		
			150	2.0	_	42	130	_	165		
				4.5	_	14	26	_	33		
				6.0	—	12	22	—	28		
	4			2.0	_	24	100	_	125		
Output disable time	t _{pLZ}	$R_L = 1 \ k\Omega$	50	4.5	_	12	20	_	25	ns	
	t _{pHZ}			6.0	_	10	17	_	21		
Input capacitance	C _{IN}	_				5	10	_	10	pF	
Output capacitance	C _{OUT}	—				10				pF	
Power dissipation capacitance	C _{PD} (Note)	_				41			_	pF	

AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$)

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

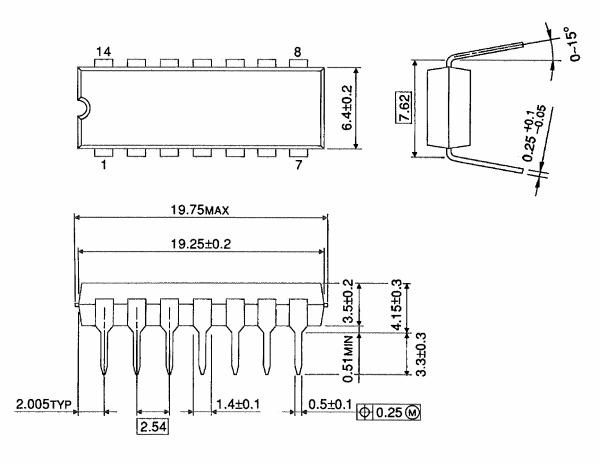
Average operating current can be obtained by the equation:

 $I_{CC} \text{ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$

Package Dimensions

DIP14-P-300-2.54

Unit : mm



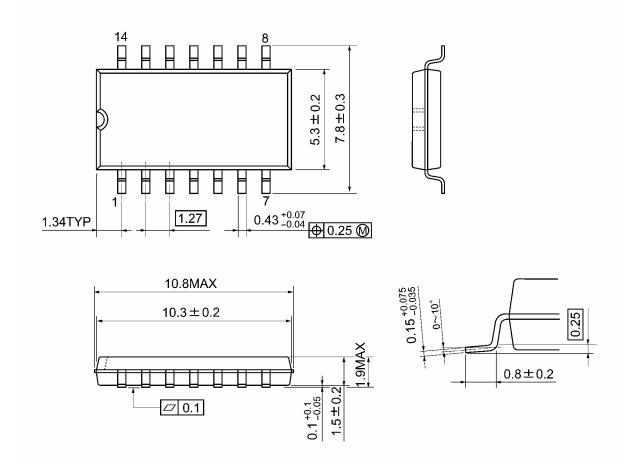
Weight: 0.96 g (typ.)



Package Dimensions

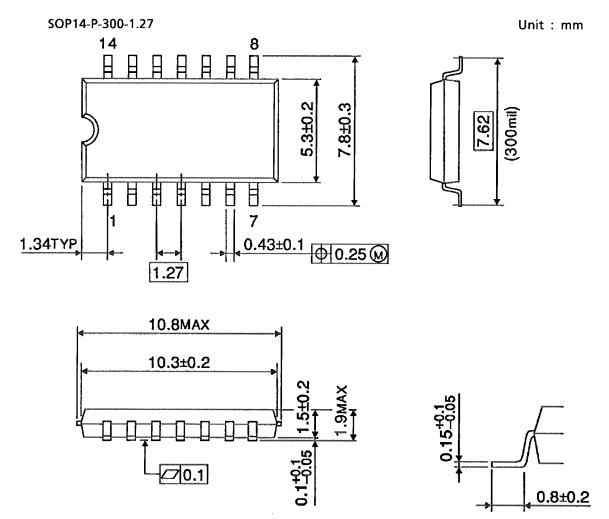
SOP14-P-300-1.27A

Unit: mm



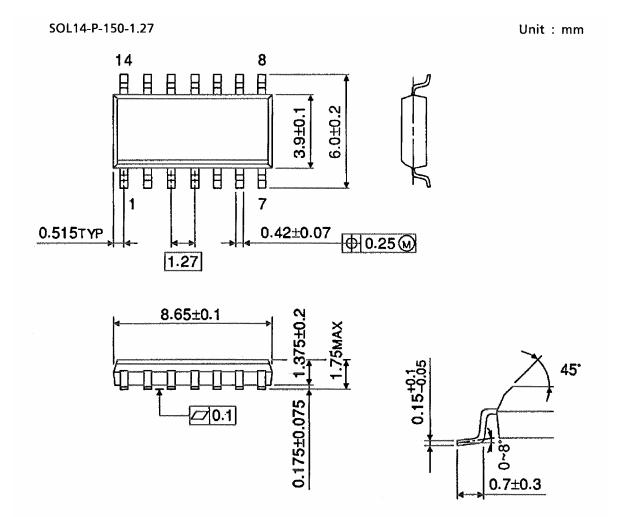
Weight: 0.18 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

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Note: Lead (Pb)-Free Packages DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27

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